IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Cheng-Lien Chiang

Assignee:

Bridge Semiconductor Corporation

Title:

OPTOELECTRONIC SEMICONDUCTOR PACKAGE DEVICE

Serial No.:

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Group Art Unit:

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Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Pursuant to Applicant's duty of disclosure under 37 C.F.R. § 1.56 and §§ 1.97-1.98, Applicant hereby submits the enclosed Form PTO-1449.

Copies of documents cited on the enclosed Form PTO-1449 are not enclosed because they were previously cited by or submitted to the U.S. Patent Office in prior U.S. Application Serial No. 10/042,812 filed January 9, 2002. The above-identified application is a continuation-in-part of the prior application.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, an admission that any of these documents, alone or in any combination, is considered to be material to patentability, an admission that any of these documents is prior art as to the above-identified application, or an admission against interest in any manner.

This Information Disclosure Statement is filed before the mailing date of a first Office Action. Accordingly, no fee is due.

Respectfully submitted,

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